



PRODUCT DATA SHEET



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Datasheet

ces Sami

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	6 m Ω	60A

Features

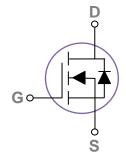
- 30V,60A, $RDS(ON) = 6m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

PPAK3X3 Pin Configuration





Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V _G s	Gate-Source Voltage	±20	V
1_	Drain Current – Continuous (Tc=25°C)	60	А
ID	Drain Current – Continuous (Tc=100°C)	38	А
I _{DM}	Drain Current – Pulsed ¹	240	А
EAS	Single Pulse Avalanche Energy ²	88	mJ
IAS	Single Pulse Avalanche Current ²	42	А
D-	Power Dissipation (Tc=25°C)	45	W
P _D	Power Dissipation – Derate above 25°C	0.36	W/°C
Т _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		62	°C/W
R _θ JC	Thermal Resistance Junction to Case		2.8	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Static State Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	BV _{DSS} Drain-Source Breakdown Voltage V _{GS} =0V , I _D =250uA		30			V
△BV _{DSS} /△T _J	△BV _{DSS} /△T _J BV _{DSS} Temperature Coefficient Reference to 25°C , I _D =1mA			0.04		V/°C
lana	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C	30V , V _{GS} =0V , T _J =25°C	1	uA	
I _{DSS}	Dialii-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =125°C			10	uA
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
Pageon	Static Drain-Source On-Resistance ³	V _{GS} =10V , I _D =20A		4.8	6	mΩ
R _{DS(ON)}	Static Dialit-Source Off-Resistance	V _{GS} =4.5V , I _D =10A		6.7	9	mΩ
V _{GS(th)}	V _{GS(th)} Gate Threshold Voltage		1.2	1.6	2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =250uA		-4		mV/°C
gfs	Forward Transconductance	V _{DS} =10V , I _D =10A		23		S

Dynamic Characteristics

Qg	Total Gate Charge ^{3,4}			11.1	18	
Q_{gs}	Gate-Source Charge ^{3, 4}	V _{DS} =15V , V _{GS} =4.5V , I _D =20A		1.85	3.8	nC
Q_{gd}	Gate-Drain Charge ^{3, 4}			6.8	12	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}			7.5	14	
Tr	Rise Time ^{3, 4}	e Time ^{3 , 4} V _{DD} =15V , V _{GS} =10V , R _G =3.3Ω		14.5	28	20
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}	I _D =15A		35.2	67	ns
T _f	Fall Time ^{3, 4}			9.6	18	
Ciss	Input Capacitance			1210	1800	
Coss	Output Capacitance	V _{DS} =25V , V _{GS} =0V , F=1MHz		190	280	pF
Crss	Reverse Transfer Capacitance			100	150	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz		2.5	5	Ω

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V _{DD} =25V, L=0.1mH, IAS=20A	20			mJ

Drain-Source Diode Characteristics

Symbol	bol Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V, Force Current			60	Α
Isм	Pulsed Source Current ³	VG=VD=UV, Force Current			240	Α
V_{SD}	Diode Forward Voltage ³	V _{GS} =0V , I _S =1A , T _J =25°C			1	V
t _{rr}	Reverse Recovery Time	Vgs=0V,Is=1A , di/dt=100A/µs				ns
Qrr	Reverse Recovery Charge	TJ=25°C				nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. $V_{DD}=25V$, $V_{GS}=10V$, L=0.1 mH, $I_{AS}=42$ A., $R_{G}=25\Omega$, L=25°C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.



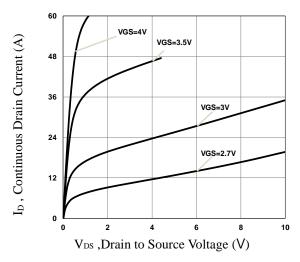


Fig.1 Typical Output Characteristics

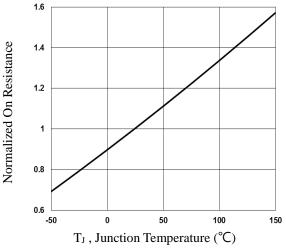


Fig.3 Normalized RDSON vs. T_J

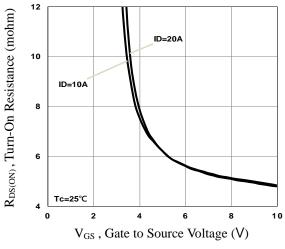


Fig.5 Turn-On Resistance vs. V_{GS}

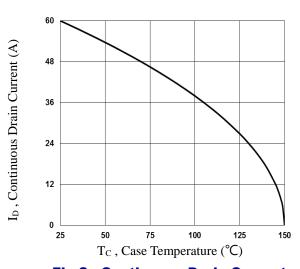


Fig.2 Continuous Drain Current vs. Tc

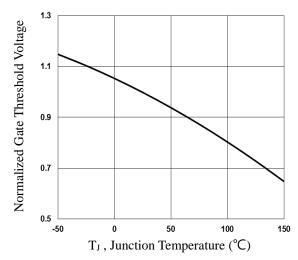


Fig.4 Normalized V_{th} vs. T_J

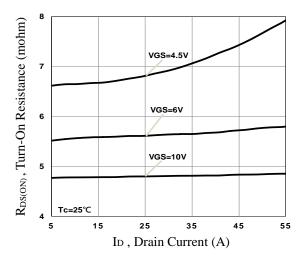


Fig.6 Turn-On Resistance vs. ID



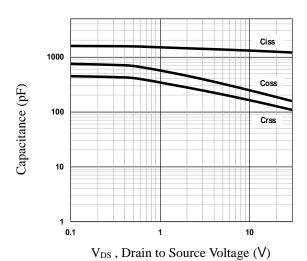


Fig.7 Capacitance Characteristics

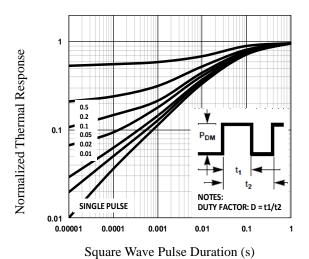


Fig.9 Normalized Transient Impedance

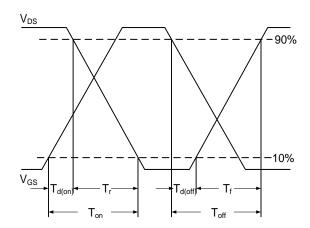


Fig.11 Switching Time Waveform

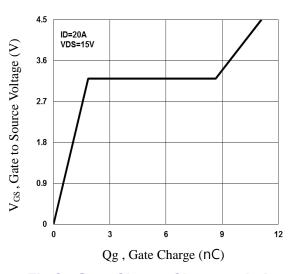


Fig.8 Gate Charge Characteristics

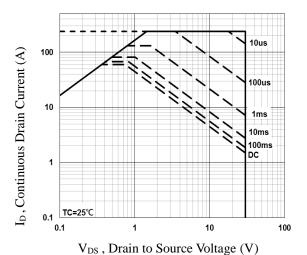


Fig.10 Maximum Safe Operation Area

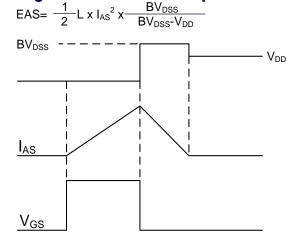
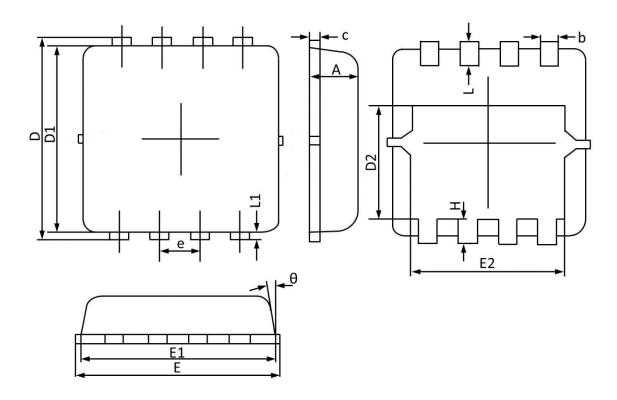


Fig.12 EAS Waveform



PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	MAX	MIN	MAX	MIN
Α	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
С	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
е	0.65	BSC	0.02	6BSC
Н	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°



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